

FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket Number 5646-81		Serial No. 10/643,208	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				Applicants: Robert J. Proebsting			
				Filing Date: August 18, 2003		Group 2812 2816	
U. S. PATENTS & PATENT APPLICATION PUBLICATIONS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1	6,593,786	7/15/03	Jung	327	158	
	2	6,586,978	7/1/03	Stief	327	158	
	3	6,584,021	6/24/03	Heyne et al.	365	194	
	4	6,539,072	3/25/03	Donnelly et al.	375	371	
	5	6,518,807	2/11/03	Cho	327	158	
	6	6,504,408	1/7/03	von Kaenel	327	158	
	7	6,501,328	12/31/02	Gauthier et al.	327	551	
	8	6,501,312	12/31/02	Nguyen	327	161	
	9	2003/0154447	8/14/03	Gauthier et al.	716	2	
	10	2003/0154417	8/14/03	Drexler	713	400	
	11	2003/0151433	8/14/03	Takai	327	158	
	12	2003/0141910	7/31/03	Reindl	327	158	
	13	2003/0117194	6/26/03	Lee	327	158	
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	15	2003/0108139	6/12/03	Jung	375	376	
	16	2003/0099321	5/29/03	Juan et al.	375	376	
	17	2003/0095009	5/22/03	Gomm et al.	331	57	
	18	2003/0094984	5/22/03	Weis et al.	327	182	
	19	2003/0090296	5/15/03	Yoo	327	12	
	20	2003/0071668	4/17/03	Starr	327	157	
	21	2003/0067335	4/10/03	von Kaenel	327	158	
	22	2003/0058014	3/27/03	Krishnamurty	327	158	
	23	2003/0052719	3/20/03	Na	327	158	

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FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	24	U.S. Patent Application No. 10/094,101, entitled <i>Apparatus and Method for Generating a Compensated Percent-of-Clock Period Delay Signal</i> , Inventor: Cesar A. Talledo, Filed March 8, 2002, 26 pages					

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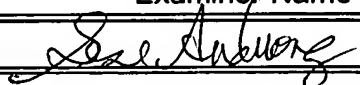

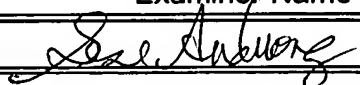

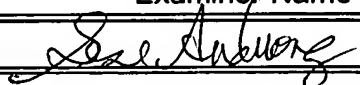
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention	Integrated Circuit Devices Having High Precision Digital Delay Lines Therein																				
<p>Application Number: 10/643208 </p> <p>Confirmation Number: 4050</p> <p>First Named Applicant: Robert Proebsting</p> <p>Attorney Docket Number: 5646-81</p> <p>Art Unit: 2812 2818</p> <p>Search string: (6326826).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6326826</td><td>2001-12-04</td><td>Lee</td><td>B1</td><td>327</td><td>161</td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td>09/07</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6326826	2001-12-04	Lee	B1	327	161	Examiner Name	Date		09/07
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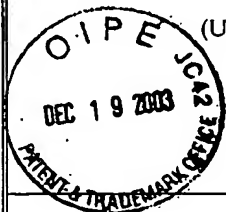
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	2	5,561,692	10/1/96	Maitland et al.	375	371	
	3	6,194,937	2/27/01	Minami	327	270	
	4	6,288,574	9/11/01	Neary	327	12	
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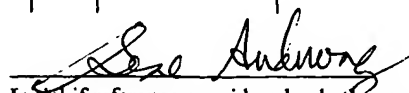
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	2	6,411,142 B1	6-25-02	Abbasi et al.	327	156	
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	17	5,847,616	12-8-98	Ng et al.	331	57	
	18	5,844,954	12-1-98	Casasanta et al.	375	373	
	19	5,828,257	10-27-98	Masleid	327	276	
	20	5,818,769	10-6-98	Tweed et al.	365	194	
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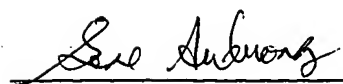
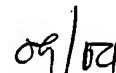


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24		Ryan, Kevin, "DDR SDRAM Functionality and Controller Read Data Capture," Micron Technology, Inc., Volume 8, Issue 3, 1999, pp. 1-24					

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